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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/791,506	03/02/2004	Rajan V. Rao	PB 04 0001 (793-04US)	2780

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THE SMALL PATENT LAW GROUP LLP
225 S. MERAMEC, STE. 725T
ST. LOUIS, MO 63105

EXAMINER	
BOKHARI, SYED M	

ART UNIT	PAPER NUMBER
2616	

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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)	
	10/791,506	RAO ET AL.	
	Examiner	Art Unit	
	SYED BOKHARI	2616	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE *3* MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *20 November 2007*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-27* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-27* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☐ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

DETAILED ACTION

Response to Amendment

1. Applicant amendment filed on November 20th, 2007 has been entered. Claim 27 has been newly added. Claims 1-27 are pending in the application.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

3. The factual inquiries set forth in *Graham* **v.** *John Deere Co.,* 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

4. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was

not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

5. Claims 1-27 are rejected under 35 U.S.C. 103(a) as being unpatentable over DeBoer et al. (US 2004/0208118 A1) in view of Anderson et al. (USP 5,838,924).

DeBoer et al. disclose an optical communication system for providing protection signaling between network elements with the following features: regarding claim 1, state maps associated with each of the line modules, the state maps storing state data that activates and deactivates the line modules (Fig. 4a, protection signaling scheme, see "table located at the module the switching is based on the information contained in the routing table" recited in paragraph 0011 lines 22-32), the state maps being updated in accordance with the lower protection scheme to perform intra-leg switching between the line modules in one of the working and protection legs (Fig. 4a, protection signaling scheme, see "the state maps are updated" recited in paragraph 0055 lines 1-16), the state map being updated in accordance with the upper protection scheme to perform inter-leg switching between a first line module in one of the working and protection legs and a second line module in another of the working and protection legs (Fig. 4a, protection signaling scheme, see "the state maps are updated" recited in paragraph 0013 lines 1-14) and the network control module performing inter-leg switching by updating the state data in the state maps for corresponding line modules in associated working and protection legs (Fig. 4a, protection signaling scheme, see "the

controller performs inter-leg switching" recited in paragraph 0012 lines 1-10); regarding claim 4, wherein the upper and lower protection schemes constitute a 1:N equipment protection scheme and a sub-network connection protection scheme (Fig. 4a, protection signaling scheme, see "provides 1:N shared mesh protection system" recited in paragraph 0013 lines 1-6); regarding claim 5, a 1:N equipment protection scheme and a UPSR protection scheme (Fig. 4a, protection signaling scheme, see "provides 1:N shared mesh protection system" recited in paragraph 0013 lines 1-6); regarding claim 6, wherein each of the state maps is stored in memory on an associated one of the line modules (Fig. 4a, protection signaling scheme, see "table located at the module the switching is based on the information contained in the routing table" recited in paragraph 0011 lines 22-32); regarding claim 9, 1:N protection schemes (Fig. 4a, protection signaling scheme, see "provides 1:N shared mesh protection system" recited in paragraph 0013 lines 1-6); regarding claim 10, wherein the state maps are stored on one of the network control module and corresponding the line modules (Fig. 4a, protection signaling scheme, see "connection table is associated with line module" recited in paragraph 0012 lines 1-10); regarding claim 11, wherein the line modules perform intra-leg switching by updating the state maps for corresponding line modules all in a common single working leg (Fig. 4a, protection signaling scheme, see "the controller performs inter-leg switching" recited in paragraph 0012 lines 1-10); regarding claim 14, wherein a first line module operates in a VT mode (Fig. 4a, protection signaling scheme, see "line module operates in VT mode" recited in paragraph 0036 lines 16-24) and a second line module operates in a DS1 mode (Fig.

4a, protection signaling scheme, see "second line in DS1 mode" recited in paragraph 0031lines 5-9); regarding claim 15, storing state data in state maps associated with each of the line modules (Fig. 4a, protection signaling scheme, see "table located at the module the switching is based on the information contained in the routing table" recited in paragraph 0011 lines 22-32), inter-leg switching between a first line module in one of the working and protection legs (Fig. 4a, protection signaling scheme, see "the state maps are updated" recited in paragraph 0055 lines 1-16), a second line module in another of the working and protection legs by updating the state maps in accordance with an inter-leg protection scheme (Fig. 4a, protection signaling scheme, see "the state maps are updated" recited in paragraph 0013 lines 1-14); regarding claim 16, further comprising intra-leg switching between the line modules in one of the working and protection legs by updating the state maps in accordance with an intra-leg protection scheme (Fig. 4a, protection signaling scheme, see "the controller performs inter-leg switching" recited in paragraph 0012 lines 1-10); regarding claim 17, 1:N protection, and UPSR protection (Fig. 4a, protection signaling scheme, see "provides 1:N shared mesh protection system" recited in paragraph 0013 lines 1-6); regarding claim 19, wherein the state maps are stored in memory on corresponding line modules (Fig. 4a, protection signaling scheme, see "table located at the module the switching is based on the information contained in the routing table" recited in paragraph 0011 lines 22-32); regarding

claim 20, further comprising generating separate intra-leg and inter-leg state maps and logically combining the intra-leg (Fig. 4a, protection signaling scheme, see "the state

maps are updated" recited in paragraph 0055 lines 1-16) and inter-leg state maps to create the state maps associated with each of the line modules (Fig. 4a, protection signaling scheme, see "the state maps are updated" recited in paragraph 0013 lines 1-14); regarding claim 21, further comprising monitoring the traffic signals for defects and performing the inter-leg switching when a defect is detected (Fig. 4a, protection signaling scheme, see "the state maps are updated" recited in paragraph 0055 lines 1-16); regarding claim 22, further comprising monitoring the traffic signals for defects and updating fault information when a defect is detected (Fig. 4a, protection signaling scheme, see "the state maps are updated" recited in paragraph 0013 lines 1-14); regarding claim 23, further comprising monitoring the traffic signals for defects and, when a defect is detected, determine whether an intra-leg protection scheme exists (Fig. 4a, protection signaling scheme, see "the state maps are updated" recited in paragraph 0055 lines 1-16); regarding claim 26, wherein a first line module operates in a VT mode (Fig. 4a, protection signaling scheme, see "line module operates in VT mode" recited in paragraph 0036 lines 16-24) and a second line module operates in a DS1 mode(Fig. 4a, protection signaling scheme, see "second line in DS1 mode" recited in paragraph 0031lines 5-9); regarding claim 27, wherein a network control module performing inter-leg switching based on the upper protection scheme by updating the state data in the state maps for corresponding line modules in associated working and protection legs (Fig. 4a, protection signaling scheme, see "the state maps are updated" recited in paragraph 0013 lines 1-14) and the line modules performing intra-leg switching based on the lower protection scheme by updating the state data in the state

maps (Fig. 4a, protection signaling scheme, see "the state maps are updated" recited in paragraph 0055 lines
1-16);

DeBoer et al. do not disclose the following features: regarding claim 1, a sub-network connection system, comprising: line modules configured to receive bridged traffic signals over individual corresponding channels, the line modules being grouped into sets at a lower protection layer, the sets of line modules being organized into working legs and protection legs at an upper protection layer, wherein the line modules are activated/deactivated based on different upper and lower protection schemes associated with the upper and lower protection layers and a network control module interconnected with the line modules; regarding claim 2, wherein the line modules constitute I/O boards and the network control module performs a switch operation between a working I/O board in the working leg and a working I/O board in the protection leg when a defect is experienced in the traffic signal; regarding claim 3, wherein the lower and upper protection schemes constitute a 1+1 protection scheme and a sub-network connection protection scheme; regarding claim 5, wherein the lower protection scheme constitutes one of a 1+1 protection scheme; regarding claim 7, further comprising a single cell switch fabric containing the network control module; regarding claim 8, further comprising a multi-cell switch fabric containing multiple switch fabrics separate and remote from the network control module; regarding claim 9, wherein the line modules generate a lower layer state map based on one of 1+1, UPSR and the network control module generates an upper layer state map based on an SNC

protection scheme, corresponding the upper and lower state maps being logically combined to create the state maps; regarding claim 13, wherein the line modules constitute a non-SONET/mixed mode combination; regarding claim 15, a method for protection switching in a sub-network connection, comprising receiving traffic signals at line modules that are grouped into sets, where the sets of line modules are organized into working legs and protection legs and activating and deactivating the line modules based on updates to the state maps; regarding claim 17, wherein the intra-leg protection scheme is one of 1+1 protection; regarding claim 18, wherein the inter-leg protection scheme is SNC protection; regarding claim 24, further comprising identifying an intra-leg protection scheme before performing the inter-leg switching.

Anderson et al. disclose a an ATM connection protection switching apparatus and method with the following features: regarding claim 1, a sub-network connection system, comprising: line modules configured to receive bridged traffic signals over individual corresponding channels (Fig. 2, point to point ATM node arrangement, see "protection switching arrangement" recited in column 1 lines 45-55), the line modules being grouped into sets at a lower protection layer (Fig. 2, point to point ATM node arrangement, see "line modules are grouped" recited in column 1 lines 45-51), the sets of line modules being organized into working legs and protection legs at an upper protection layer (Fig. 2, point to point ATM node arrangement, see "line modules are grouped" recited in abstract lines 1-12), wherein the line modules are activated/deactivated based on different upper and lower protection schemes associated with the upper and lower protection layers (Fig. 2, point to point ATM node

arrangement, see "modules are activated/deactivated" recited in column 4 lines 50-67 and column 5 lines 1-12) and a network control module interconnected with the line modules (Fig. 2, point to point ATM node arrangement, see "controller is connected with the modules" recited in column 8 lines 60-67 and column 9 lines 1-5); regarding claim 2, wherein the line modules constitute I/O boards and the network control module performs a switch operation between a working I/O board in the working leg and a working I/O board in the protection leg when a defect is experienced in the traffic signal (Fig. 2, point to point ATM node arrangement, see "controller performs a switch operation" recited in column 5 lines 1-12); regarding claim 3, wherein the lower and upper protection schemes constitute a 1+1 protection scheme and a sub-network connection protection scheme (Fig. 2, point to point ATM node arrangement, see "it is applicable to 1+1 mesh and ring protection switching" recited in column 1 lines 55-59); regarding claim 5, wherein the lower protection scheme constitutes one of a 1+1 protection scheme (Fig. 2, point to point ATM node arrangement, see "it is applicable to 1+1 mesh and ring protection switching" recited in column 1 lines 55-59); regarding claim 7, further comprising a single cell switch fabric containing the network control module (Fig. 9, logical configuration of switch fabric, see "bidirectional line switched ring" recited in column 10 lines 5-12); regarding claim 8, further comprising a multi-cell switch fabric containing multiple switch fabrics separate and remote from the network control module (Fig. 9, logical configuration of switch fabric, see "bidirectional line switched ring" recited in column 10 lines 5-12); regarding claim 9, wherein said line modules generate a lower layer state map based on one of 1+1, UPSR (Fig. 2, point to

point ATM node arrangement, see "it is applicable to 1+1 mesh and ring protection switching" recited in column 1 lines 55-59) and the network control module generates an upper layer state map based on an SNC protection scheme, corresponding the upper and lower state maps being logically combined to create the state maps (Fig. 2, point to point ATM node arrangement, see "inter-leg protection scheme" recited in column 1 lines 55-59); regarding claim 12, wherein the line modules are housed within one of a cross-connect and add/drop multiplexer and support one of uni- directional and bidirectional switching (Fig. 2, point to point ATM node arrangement, see "controller performs a switch operation" recited in column 5 lines 1-12); regarding claim 13, wherein the line modules constitute a non-SONET/mixed mode combination (Fig. 7, details of line terminal units, see "line terminals are non-SONET/mix mode" recited in column 9 lines 6-14); regarding claim 15, a method for protection switching in a sub-network connection (Fig. 2, point to point ATM node arrangement, see "protection switching arrangement" recited in column 1 lines 45-55), comprising receiving traffic signals at line modules that are grouped into sets (Fig. 2, point to point ATM node arrangement, see "line modules are grouped" recited in column 1 lines 45-51), where the sets of line modules are organized into working legs and protection legs (Fig. 2, point to point ATM node arrangement, see "line modules are grouped" recited in abstract lines 1-12) and activating and deactivating the line modules based on updates to the state maps (Fig. 2, point to point ATM node arrangement, see "modules are activated/deactivated" recited in column 4 lines 50-67 and column 5 lines 1-12); regarding claim 17, wherein the intra-leg protection scheme is one of 1+1 protection

(Fig. 2, point to point ATM node arrangement, see "it is applicable to 1+1 mesh and ring protection switching" recited in column 1 lines 55-59); regarding claim 18, wherein the inter-leg protection scheme is SNC protection (Fig. 2, point to point ATM node arrangement, see "inter-leg protection scheme" recited in column 1 lines 55-59); regarding claim 24, further comprising identifying an intra-leg protection scheme before performing the inter-leg switching (Fig. 2, point to point ATM node arrangement, see "modules are activated/deactivated" recited in column 4 lines 50-67 and column 5 lines 1-12); regarding claim 25, wherein the line modules constitute a non-SONET/mixed mode combination (Fig. 7, details of line terminal units, see "line terminals are non-SONET/mix mode" recited in column 9 lines 6-14);

It would have been obvious to one of the ordinary skill in the art at the time of invention to modify the system of DeBoer et al. by using the features, as taught by Anderson et al., in order to provide a sub-network connection system, comprising: line modules configured to receive bridged traffic signals over individual corresponding channels, the line modules being grouped into sets at a lower protection layer, the sets of line modules being organized into working legs and protection legs at an upper protection layer, wherein the line modules are activated/deactivated based on different upper and lower protection schemes associated with the upper and lower protection layers and a network control module interconnected with the line modules, the line modules constitute I/O boards and the network control module performs a switch operation between a working I/O board in the working leg and a working I/O board in the protection leg when a defect is experienced in the traffic signal, the lower and upper

protection schemes constitute a 1+1 protection scheme and a sub-network connection protection scheme, the lower protection scheme constitutes one of a 1+1 protection scheme, comprising a single cell switch fabric containing the network control module, a multi-cell switch fabric containing multiple switch fabrics separate and remote from the network control module, the line modules generate a lower layer state map based on one of 1+1, UPSR and the network control module generates an upper layer state map based on an SNC protection scheme, corresponding the upper and lower state maps being logically combined to create the state maps, the line modules constitute a non-SONET/mixed mode combination, for protection switching in a sub-network connection, comprising receiving traffic signals at line modules that are grouped into sets, where the sets of line modules are organized into working legs and protection legs and activating and deactivating the line modules based on updates to the state maps, the intra-leg protection scheme is one of 1+1 protection, the inter-leg protection scheme is SNC protection and identifying an intra-leg protection scheme before performing the inter-leg switching.

Response to Arguments

6. Applicant's arguments with respect to claims 1-27 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to SYED BOKHARI whose telephone number is (571)270-

3115. The examiner can normally be reached on Monday through Friday 8:00-17:00 Hrs..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kwang B. Yao can be reached on (571) 272-3182. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Syed Bokhari/

Examiner, Art Unit 2616

6/23/2008

/Kwang B. Yao/

Supervisory Patent Examiner, Art Unit 2616